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APPLICATION NO.	FILING	DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/644,484	08/20	/2003	Trung V. Le	10383US01	7391
	7590 09/20/2007		EXAMINER		
Attention: Eric Imation Corp.	D. Levinsoi	n	GETACHEW, ABIY		
Legal Affairs)			ART UNIT	PAPER NUMBER
P.O. Box 64898 St. Paul, MN 55164-0898				2841	
				MAIL DATE	DELIVERY MODE
				09/20/2007	PAPER ·

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Application No. 10/644.484 Examiner Ahly Getachew 2841 ART Unit Aby Getachew 2841 AS HORTENED STATUTORY PERIOD FOR REDY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MALLING DATE OF THIS COMMUNICATION. **STATUTIORY PERIOD FOR REDY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MALLING DATE OF THIS COMMUNICATION. **If NO period for remy is available under the processor of 17 CPR 11 N(6). In re-event, however, may a veryly be larroy thed after SIX (6), MONTHS from the maining sale of this communication. **If NO period for regy is applied stokes the maximum statistics period of apply and will expire SIX (6), MONTHS from the maining sale of this communication. **If NO period for regy is applied stokes the maximum statistics period of apply and will expire SIX (6), MONTHS from the maining sale of this communication. **If NO period for regy is applied stokes the maximum statistics period of apply and will expire SIX (6), MONTHS from the maining sale of this communication. **If NO period for regy is applied stokes the maximum statistics period of applied stoke of this communication, even if timely filled, may reduce any owners planet term adjustment. See 37 CPR 1.704(p). **Status** **If NO period for regy is applied stokes the maximum statistics period of this communication, even if timely filled, may reduce any owners planet term adjustment. See 37 CPR 1.704(p). **Status** **If NO period for regy is applied and the maximum statistics period of this communication. **If NO period for regy is applied to the maximum statistics period of this communication. **If NO period for regy is applied to the maximum statistics. **If NO period for regy is applied to the maximum statistics. **If NO period for regy is applied to the maximum statistics. **If NO period for regy is applied to the maximum statistics. **If NO period for regy is applied to the maximum statistics. **If NO period for regy is applied to the priod for the priod for the priod									
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2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	2) Notic	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail	l Date					
3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 01 01. 5) Notice of Informal Patent Application 6) Other:				al Patent Application					

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission *filed on 07/13/2007* has been entered.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 7-9, 11-13, 15, 19-20, 23, 25, 27-30 are rejected under 35 U.S.C. 102(b) as being anticipated by Kaneko Yoshio (2002JP-2002-084930 or US2003/0221066).

Regarding claim 1 Kaneko Yoshio discloses, a memory card (See figure 3) comprising: a memory (28, 32); a first connector (25) electrically couple to the memory (28) and conforming to a first connector (25) standard; a second connector (27) electrically coupled to the memory (32) and conforming to a second connector (26), wherein the first connector (25) standard comprises a host computer connector (HCC)

standard and the second connector (27) standard comprises a device communication connector (DCC) standard; and a controller (29, 35) that controls the memory (28, 32) and controls output via the first connector (25) and the second connector (27), wherein the first (25) and second (27) connectors are electrically coupled to the memory (28, 32) through the controller (29, 35) and wherein the controller (29, 35) comprises a memory controller (30, 34) integrated with a first connector controller (not show) conforming to the first connector (25) standard and integrated with a second connector controller (not show) conforming to the second connector (27) standard, wherein at least one of the first and the second connector comprises a retractable connector (23) that can be positioned in a extended position and a retracted position (See figure 3).

Regarding claims 7,25 Kaneko Yoshio discloses, the memory card (See figures 2-4) wherein, the HCC comprises a standard selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a PC Card standard, a Card Bus standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Universal Serial Bus (USB) standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Universal Serial Bus 2 (USB2) standard (see column 3, paragraph 0029 and column 6, paragraph 0066), an IEEE 1394 Fire Wire standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Small Computer System Interface (SCSI) standard (see column 3, paragraph 0029 and column 6, paragraph 0029 an

column 6, paragraph 0066), a Peripheral Component Interconnect (PCI) standard (see column 3, paragraph 0029 and column 6, paragraph 0066), and a conventional serial or parallel standard (see column 3, paragraph 0029 and column 6, paragraph 0066); and the DCC comprises a standard selected from a group consisting of: a Compact Flash standard, a Smart Media standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Multimedia Card standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Secure Digital standard (see column 3, paragraph 0029 and column 6, paragraph 0066), a Memory Stick Standard (see column 3, paragraph 0029 and column 6, paragraph 0066), and an xD standard (see column 3, paragraph 0029 and column 6, paragraph 0066).

Regarding claim 8 Kaneko Yoshio discloses the memory card (See figures 2-4) wherein the first connector (explain in claim 1) is disposed (see figure 2) on a different side of the memory card (explained in claim 1) than the second connector (explain in claim 1).

Regarding claim 9 Kaneko Yoshio discloses the memory card (See figures 2-4) wherein the first connector (explain in claim 1) is disposed (see figure 2) on an opposite side of the memory card (explain in claim 1) relative to the second connector (explain in claim 1).

Regard claim 11 Kaneko Yoshio discloses the memory card (See figures 3) further comprising: a housing defining a slot for the retractable connector; and a first electrical contact on the retractable connector and a second electrical contact within the slot, wherein the first electrical contact couples to the second electrical contact when the

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retractable connector is extended from the slot (elements 25, 27 are within the connector 36).

Regarding claim 12 Kaneko Yoshio discloses the memory card (See figure3) wherein the first connector (explain in claim 1) is disposed on the same side of the memory card (25 is on the same side with 28) as the second connector (explain above).

Regarding claim 13 Kaneko Yoshio discloses, the memory card (See figure 3) wherein a set of electrical contact elements of the first connector (explain above) comprise a subset of a set of electrical contact elements (rectangular portion of 25 and 27) of the second connector (explain in claim 1).

Regarding claim 15 Kaneko Yoshio discloses, a memory (28, 32); a first connector (25) electrically coupled to the memory (28, 32) and conforming to a first connector (25) standard; a second connector (27) electrically couple to the memory (32) and conforming to a second connector (27) standard wherein the first connector (25) standard comprises a host computer connector (HCC) standard and the second connector (27) standard comprises a device communication connector (DCC) standard; a first controller (29) electrically coupled to the memory (28) and the first connector (25), the first controller (29) controlling the memory (28) and output via the first connector (25); wherein the first controller (29) comprises a memory controller (30) integrated with a first connector controller (not show) conforming to the first connector (25) standard; and a second controller (35) electrically coupled to the second connector (27) and the first controller (29), the second controller (35) controlling output via the second connector (27) and conforming to the second connector (27) standard, therein the first

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connector (25) is electrically coupled to the memory (28) through the first controller (29), and the second connector (27) is electrically coupled to the memory (32) through the second controller (35) and the first controller (29), wherein at least one of the first and the second connector (27) comprises a retractable connector (23) that can be positioned in a extended position and a retracted position (See figure 3).

Regard claim 19 Kaneko Yoshio discloses the memory card further comprising a third connector (24) electrically coupled to the memory (28) and conforming to a third connector (24) standard.

Regard claim 20 Kaneko Yoshio discloses the memory card further comprising a fourth connector (26) electrically coupled to the memory (32) and conforming to a fourth connector (26) standard.

Regarding claim 23 Kaneko Yoshio discloses, a system (See figure 3) comprising: a first device (21) including a first electrical contact (rectangular portion of 25) for receiving a connector that conform to a first connector (25) standard a second device (22) including a second electrical contact (rectangular portion of 27) for receiving a connector that conforms to a second connector (27) standard; and a memory card (all elements of figure 3) including: a memory (28, 32), a first connector (25) conforming to the first connector (25) standard such that the first connector (25) can be received by the first electrical contact (rectangular portion of 25) of the first device (21), a second connector (26) conforming to the second connector (26) standard such that the second connector (27) can be received by the second electrical contact (rectangular portion of 27) of the second device (22), wherein the first connector (25)

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stand comprises a host. Computer connector (HCC) standard and the second connector (27) standard comprises a device communication connector (DCC) standard, and a controller (29) that controls the memory and controls output via the first connector (25) and the second connector (27) wherein the first (25) and second (27) connectors are electrically coupled to the memory through the controller (29, 35) and wherein the controller (29, 35) comprises a memory controller (30, 34) integrated with a first connector controller (connector not show)conforming to the first connector (25) standard and integrated with a second connector controller (connector not show) conforming to the second connector (27) standard, wherein at least one of the first and the second connector comprises a retractable connector (23) that can be positioned in a extended position and a retracted position (See figure 3).

Regarding claim 27 Kaneko Yoshio discloses, a system (See figure 3) comprising: a first device (21) including a first electrical contact (rectangular portion of 25) for receiving a connector that conforms to a first connector (25) standard; a second device (22) including a second electrical contact (rectangular portion of 27) for receiving a connector that conforms to a second connector (27) standard; and a memory card (21, 22) including: a memory (28, 32), a first connector (25) conforming to the first connector (25) standard such that the first connector (25) can be received by the first electrical contact (rectangular portion of 25) of the first device (21), a second connector (27) conforming to the second connector (27) standard such that the second connector (27) can be received by the second electrical contact (rectangular portion of 27) of the second device (22), wherein the first connector (25) standard comprises a

host computer connector (HCC) standard and the second connector (27) standard comprises a device communication connector (DCC) standard, a first controller (29) electrically coupled to the memory (28) and the first connector (25), the first controller (29) controlling the memory (28) and output via the first connector (25), wherein the first controller (28) comprises a memory controller (30) integrated with a first connector controller (not show) conforming to the first connector (25) standard, and a second controller (35) electrically coupled to the second connector (27) and the first controller (35), the second controller (35) controlling output via the second connector (27) and conforming to the second connector (27) standard, wherein the first connector (25) is electrically couple to the memory (28) through the first controller (29), and the second connector (27) is electrically couple to the memory (32) through second controller (35) and the first controller (29) wherein at least one of the first and the second connector comprises a retractable connector (23) that can be positioned in a extended position and a retracted position (See figure 3).

Regarding claim 28 Kaneko Yoshio discloses, a memory card (See figure 3) comprising: a memory (28, 32); a first connector (25) electrically couple to the memory (28) and conforming to a first connector (25) standard; a second connector (27) electrically coupled to the memory (32) and conforming to a second connector (26), wherein the first connector (25) standard comprises a host computer connector (HCC) standard and the second connector (27) standard comprises a device communication connector (DCC) standard; one or more controllers that control the memory (28,32) and control output via the first connector and the second connector (25,27), wherein the first

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and second connectors (25,27) are formed along a common side of the memory card (explained in claim 1) and wherein electrical contacts of the second connector (27) comprise a subset of electrical contacts of the first connector (25).

Regarding claim 29 Kaneko Yoshio discloses, wherein the second connector (27) comprises a retractable connector (23) that can be positioned in an extended position and a retracted position. (See figure 3).

Regarding claim 30 Kaneko Yoshio discloses, wherein the electrical contacts of the second connector (27) comprise movable contacts form the second connector (27) when the second connector is in the extended position and wherein the electrical contacts of the second connector (27) comprise a subset of the electrical contacts of the first connector when the second connector is in the retracted position (23). (See figure 3)

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Abiy Getachew whose telephone number is (571) 272 6932. The examiner can normally be reached on Monday to Friday 8Am to 4:30Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean A. Reichard can be reached on (571) 272 1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Abiy Getachew

Examiner

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A.G. September 15, 2007

TUAN T. DINH PRIMARY EXAMINER